

# **SWAMI VIVEKANAND UNIVERSITY, SIRONJA, SAGAR (M.P.)**



## **SCHEME**

**For**

### **DIPLOMA IN ENGINEERING (3 YEAR) ELECTRONICS & COMMUNICATION ENGINEERING**

**Course Code: DEC**

Department of Electronics & Communication  
Engineering

Faculty of Engineering

Duration of Course : 3 Year

Examination Mode : Semester

Examination System : Grading

Swami Vivekanand University, Sironja Sagar (M.P.)

2016-2017



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering

Department of Electronics & Communication Engg.

Scheme of Course : Diploma in Engg.(3 Year) – Electronics & Comm. Engg. Course Code : **DEC**

Semester/Year – 3<sup>rd</sup> Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks										Duration of Theory Exam
		L	T	P		Theory					Practical			Grand Total (H= D+G)		
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (G= E+F)	
						Max (A)	Min	TW (B)	MST (C)		Max (E)	Min				
DEC-0301	Electronic Components & Materials	3	1	2	6	70	22	10	20	100	-	-	-	-	100	3 Hrs
DEC-0302	Electronic Devices & Circuits	3	1	2	6	70	22	10	20	100	30	9	20	50	150	3 Hrs
DEC-0303	Basic Electrical Engineering	3	1	2	6	70	22	10	20	100	30	9	20	50	150	3 Hrs
DEC-0304	Network Analysis	3	1	2	6	70	22	10	20	100	30	9	20	50	150	3 Hrs
DEC-0305	Digital Electronics	3	1	2	6	70	22	10	20	100	30	9	20	50	150	3 Hrs
DEC-0306	Professional Activities	3	1		4	-	-	-	-	-	-	-	-	-	--	-
	<b>Total</b>	<b>18</b>	<b>6</b>	<b>10</b>	<b>34</b>	<b>350</b>	<b>-</b>	<b>50</b>	<b>100</b>	<b>500</b>	<b>120</b>	<b>-</b>	<b>80</b>	<b>200</b>	<b>700</b>	



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering

Department of Electronics & Communication Engg

Scheme of Course : Diploma in Engg.(3 Year) – Electronics & Comm. Engg    Course Code : **DEC**                      Semester/Year – 4<sup>th</sup> Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks											Duration of Theory Exam
		L	T	P		Theory					Practical			Grand Total (H=D+G)			
						End Sem.		Internal		Total (D=A+B+C)	End Sem.		Internal		Total (G=E+F)		
						Max (A)	Min	TW (B)	MST (C)		Max (E)	Min				LW (F)	
DEC-0401	Linear Integrated Circuits	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs	
DEC-0402	Microprocessor & Microcontroller	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs	
DEC-0403	Electronic Measurements	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs	
DEC-0404	Communication Engineering	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs	
DEC-0405	Elective – I (list given below)	3	1	-	4	70	22	10	20	100	-	-	-	-	100	3 Hrs	
DEC-0406	Professional Activities	3	1		4	-	-	-	-	-	-	-	-	-	--	-	
	<b>Total</b>	<b>18</b>	<b>6</b>	<b>8</b>	<b>32</b>	<b>350</b>	<b>-</b>	<b>50</b>	<b>100</b>	<b>500</b>	<b>120</b>	<b>-</b>	<b>80</b>	<b>200</b>	<b>700</b>		

**Elective – I**

DEC0405(A) : Entrepreneurship

DEC0405(B) : Marketing Management



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination



Faculty of Engineering

Department of Electronics & Comm. Engg

Scheme of Course : Diploma in Engg.(3 Year) – Electronics & Comm. Engg.

Course Code : **DEC**

Semester/Year – 5<sup>th</sup>Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks										Duration of Theory Exam
		L	T	P		Theory					Practical			Grand Total (H= D+G)		
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (G= E+F)	
						Max (A)	Min	TW (B)	MST (C)		Max (E)	Min				
DEC-0501	Instrumentation & Control System	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0502	Data Communication & Networks	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0503	Industrial Electronics	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0504	Antenna And Microwave Engineering	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0505	Electronic Workshop Practice	-	2	2	4	-	-	-	-	-	60	18	40	100	100	-
DEC-0506	Professional Activities	3	1	-	4	-	-	-	-	-	-	-	-	-	-	-
	<b>Total</b>	15	7	10	32	280	-	40	80	400	180	-	120	300	700	-



# Swami Vivekanand University, Sagar (M.P.)

## Scheme of Examination

Faculty of Engineering

Department of Electronics & Communication Engg.

Scheme of Course : Diploma in Engg.(3 Year) – Electronics & Comm. Engg. Course Code : **DEC**

Semester/Year – 6<sup>th</sup>Sem

Paper / Subject Code	Title of the Paper / Subject	Credit Allotted			Total Credit	Distribution of Marks										Duration of Theory Exam
		L	T	P		Theory					Practical			Grand Total (H= D+G)		
						End Sem.		Internal		Total (D= A+B+C)	End Sem.		Internal		Total (G= E+F)	
						Max (A)	Min	TW (B)	MST (C)		Max (E)	Min				
DEC-0601	Consumer Electronics	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0602	Advanced Communication	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0603	Elective – II (List given below)	3	1	2	6	70	22	10	20	100	30	09	20	50	150	3 Hrs
DEC-0604	Project	-	-	8	8	-	-	-	-	-	100	31	100	200	200	-
DEC-0605	Seminar	-	-	2	2	-	-	-	-	-	-	-	50	50	50	-
DEC-0606	Professional Activities	3	1	-	4	-	-	-	-	-	-	-	-	-	-	-
	<b>Total</b>	12	4	16	32	210	-	30	60	300	190	-	210	400	700	-

List of Elective Papers –

**Elective – II**

DEC-0603(A) – Computer Programming

DEC-0603(B) – Programmable Logic Controllers