

# **SWAMI VIVEKANAND UNIVERSITY, SIRONJA, SAGAR (M.P.)**



## **SYLLABUS**

**For**

**BACHELOR OF TECHNOLOGY (B.Tech)  
COMPUTER SCIENCE & ENGINEERING**

**Course Code : BTCSE**

Department of Computer Science & Engineering

Faculty of Engineering

Duration of Course : 4 Years

Examination Mode : Semester

Examination System : Grading

Swami Vivekanand University, Sironja Sagar (M.P.)  
2016-2017



# Swami Vivekanand University, Sagar(M.P.)



Faculty : Engineering

Department : Computer Science & Engineering

Scheme of Course : B.Tech.-Computer Science

Course Code : BTCSE

WEF 2016-2017

## Semester III Branch - Computer Science And Engineering SubjectWiseDistributionofMarksAndCorrespondingCredits

S.N	Title of the Paper / Subject	Title of the Paper / Subject	Maximum Marks Allotted											Credits				Remark
			Theory Slot					Practical Slot						GrandTotal Marks ( I = D+H)	Allotted Subject wise			
			End		Internal		Total	End Sem.		Internal		Total (H= E+F+G)	Period per week			Total Credits		
			Max (A)	Min	TW (B)	MST (C)	(D=A+B+C)	Max (E)	Min	LW (F)	PQ (G)		L		T		P	
1	BTCSE-0301	Mathematics -II	70	22	20	10	100	-		-		-	100	3	1	-	4	
2	BTCSE-0302	Discrete Structures	70	22	20	10	100	-		-		-	100	3	1	-	4	
3	BTCSE-0303	Digital Circuit & System	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
4	BTCSE-0304	Electronics Devices & Circuits	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
5	BTCSE-0305	Data Structures	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
6	BTCSE-0306	Computer Programming (Java Technologies)	-		-	-		30	09	10	10	50	50	0	0	2	2	Grand Total
7	BTCSE-0307	Self Study (Internal Assessment)	-		-	-		-		-	50	50	50	0	0	2	2	
8	BTCSE-0308	Seminar / Group Discussion (Internal Assessment)	-		-	-		-		-	50	50	50	0	0	2	2	
		Total	350		100	50	500	120	-	40	140	300	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

L: Lecture - T: Tutorial - P: Practical



# Swami Vivekanand University, Sagar(M.P.)



Faculty : Engineering

Department : Computer Science & Engineering

Scheme of Course : B.Tech.-Computer Science

Course Code : BTCSE

WEF 2016-2017

## Semester IV Branch - Computer Science And Engineering SubjectWiseDistributionOfMarksAndCorrespondingCredits

S.N	Title of the Paper / Subject	Title of the Paper / Subject	Maximum Marks Allotted										GrandTotal Marks ( I = D+H)	Credits Allotted Subject wise			Remark	
			Theory Slot					Practical Slot						Period per week				Total Credits
			End		Internal		Total	End Sem.		Internal		Total (H= E+F+G)						
			Max (A)	Min	TW (B)	MST (C)	(D=A+B+C)	Max (E)	Min	LW (F)	PQ (G)			L	T	P		
1	BTCSE-0401	Mathematics III	70	22	20	10	100	-		-	-		100	3	1	-	4	
2	BTCSE-0402	Computer System Organization	70	22	20	10	100	-		-	-		100	3	1		4	
3	BTCSE-0403	Object Oriented Technology	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
4	BTCSE-0404	Analysis & Design of Algorithm	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
5	BTCSE-0405	Analog & Digital Communication	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
6	BTCSE-0406	Computer Programming –IV (.Net Technologies)	-	-	-	-	-	30	09	10	10	50	50	0	0	2	2	
7	BTCSE-0407	Self Study (Internal Assessment)	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	
8	BTCSE-0408	Seminar / Group Discussion (Internal Assessment)	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	<b>Grand Total</b>
		<b>Total</b>	350	-	100	50	500	120	-	40	140	300	800	15	5	12	32	800

**MST: Mid Semester Tests Taken at Least twice Per Semester**

**L: Lecture - T: Tutorial - P: Practical**



# Swami Vivekanand University, Sagar(M.P.)



Faculty : Engineering

Department : Computer Science & Engineering

Scheme of Course : B.Tech.-Computer Science

Course Code : BTCSE

WEF 2016-2017

## Semester VV Branch - Computer Science And Engineering SubjectWiseDistributionOfMarksAndCorrespondingCredits

S.N	Title of the Paper / Subject	Title of the Paper / Subject	Maximum Marks Allotted										GrandTotal Marks ( I = D+H)	Credits			Remark	
			Theory Slot					Practical Slot						Allotted Subject wise				
			End		Internal		Total	End Sem.		Internal		Total (H= E+F+G)		Period per week				Total Credits
			Max (A)	Min	TW (B)	MST (C)	(D=A+B+C)	Max (E)	Min	LW (F)	PQ (G)			L	T	P		
1	BTCSE-0501	Data Communication	70	22	20	10	100	-	-	-	-	-	100	3	1	-	4	
2	BTCSE-0502	Operating System	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
3	BTCSE-0503	Data Base Management	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
4	BTCSE-0504	Computer Graphics & Multimedia	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
5	BTCSE-0505	Theory of Computation	70	22	20	10	100	-	-	-	-	-	100	3	1	-	4	
6	BTCSE-0506	Computer Programming V (Unix/Linux Lab)	-	-	-	-	-	30	09	10	10	50	50	0	0	2	2	
7	BTCSE-0507	Self Study (Internal	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	
8	BTCSE-0508	Seminar / Group Discussion	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	Grand Total
		Total	350	-	100	50	500	120	-	40	140	300	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

L: Lecture - T: Tutorial - P: Practical





# Swami Vivekanand University, Sagar(M.P.)



Faculty : Engineering

Department : Computer Science & Engineering

Scheme of Course : B.Tech.-Computer Science

Course Code : BTCSE

WEF 2016-2017

## Semester VII Branch - Computer Science And Engineering SubjectWiseDistributionOfMarksAndCorrespondingCredits

S.N	Title of the Paper / Subject	Title of the Paper / Subject	Maximum Marks Allotted											Credits			Remark	
			Theory Slot					Practical Slot						GrandTotal Marks ( I = D+H)	Allotted Subject wise			
			End		Internal		Total	End Sem.		Internal		Total (H= E+F+G)	Period per week			Total Credits		
			Max (A)	Min	TW (B)	MST (C)	(D=A+B+C)	Max (E)	Min	LW (F)	PQ (G)		L		T			P
1	BTCSE-0701	Compiler Design	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
2	BTCSE-0702	Distributed System	70	22	20	10	100	-	-	-	-	-	100	3	1	-	4	
3	BTCSE-0703	Cloud Computing	70	22	20	10	100	30	09	10	10	50	150	3	1	2	6	
4	BTCSE-0704	Information Storage &	70	22	20	10	100	-	-	-	-	-	100	3	1	-	4	
5	BTCSE 0705	Elective –I (Refer Table	70	22	20	10	100	-	-	-	-	-	100	3	1	-	4	
6	BTCSE-0706	Industrial Training (2Week)	-	-	-	-	-	30	09	10	10	50	50	0	0	2	2	
7	BTCSE-0707	Major Project –I ( Literature review & Synopsis)	-	-	-	-	-	30	09	10	10	50	50	0	0	2	2	
8	BTCSE-0708	Self Study	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	Grand Total
9	BTCSE-0709	Seminar /Group Discussion	-	-	-	-	-	-	-	-	50	50	50	0	0	2	2	
		<b>Total</b>	350	-	100	50	500	120	-	40	140	300	800	15	5	12	32	800

MST: Mid Semester Tests Taken at Least twice Per Semester

L: Lecture - T: Tutorial - P: Practical

**Elective –I**

BTCSE 0705(A) –Network & Web Security

BTCSE 0705(C) – Embedded Computer System

BTCSE 0705(B) – Simulation & Modeling

BTCSE 0705(D) – Real Time Fault Tolerant Systems

